EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((input adj device) and (output adj device) and processor and address and (command adj bus$2) and (bi adj stable adj relay)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/01 14:00
L2	0	((memory adj cell adj array) and (signal adj buses) and (external device) and (non adj volatile adj configuration adj circuit) and (power adj source)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/01 14:01
L3	181	gomm.inv.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/01 14:01
L4	4325	tyler.inv.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/01 14:01
L5	71	3 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/01 14:01

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